

100

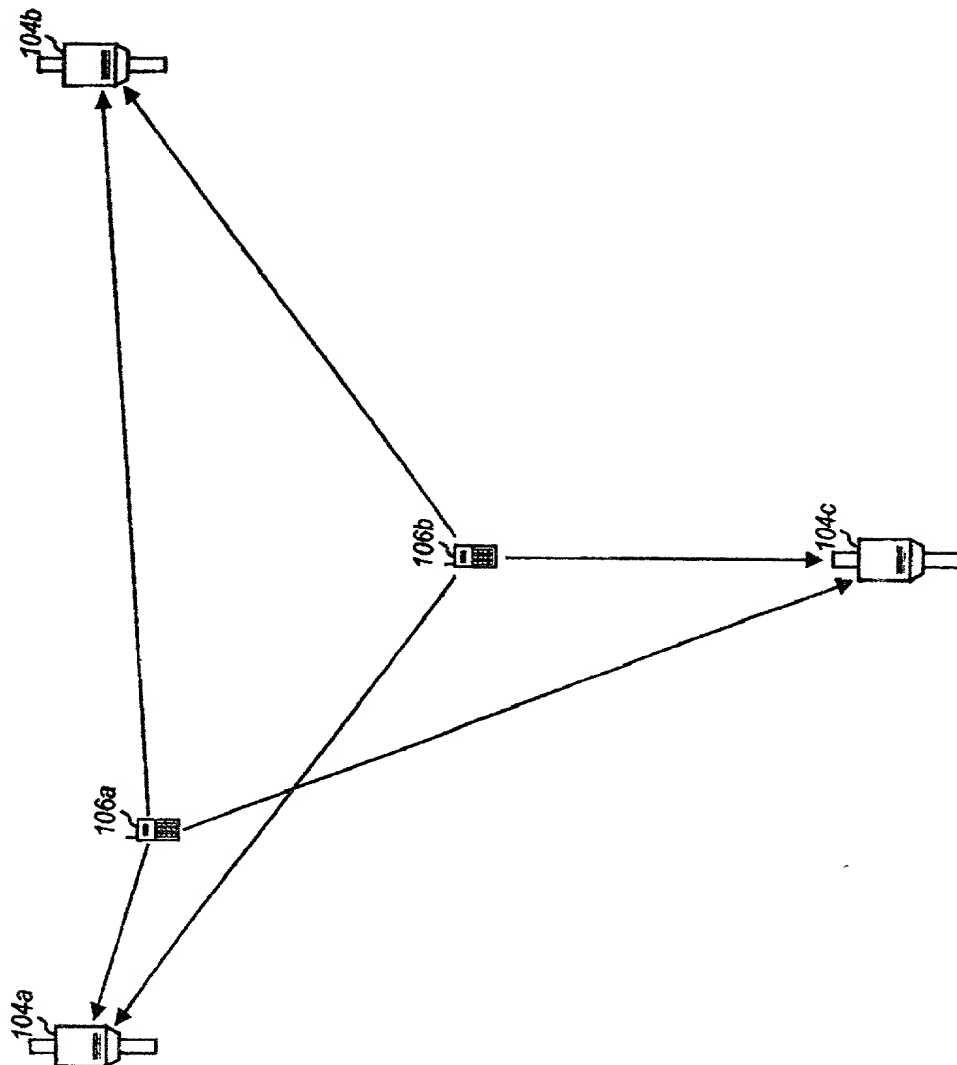
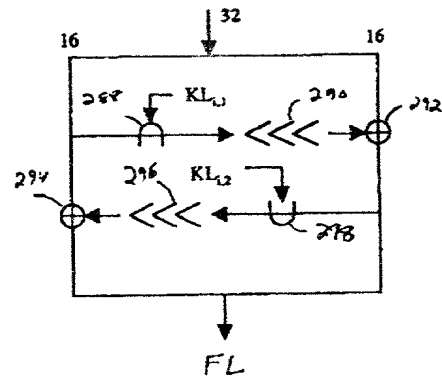
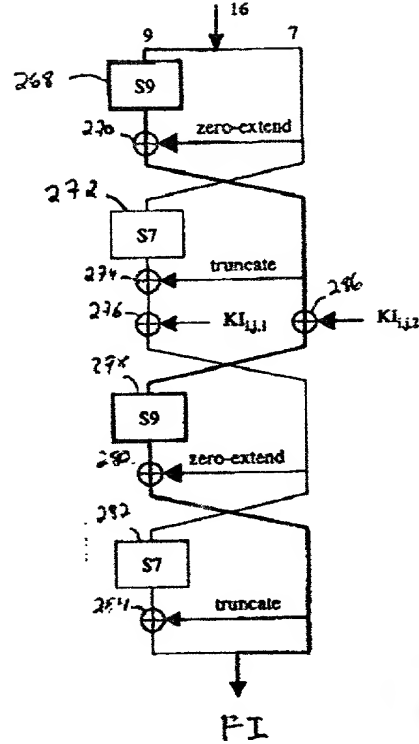
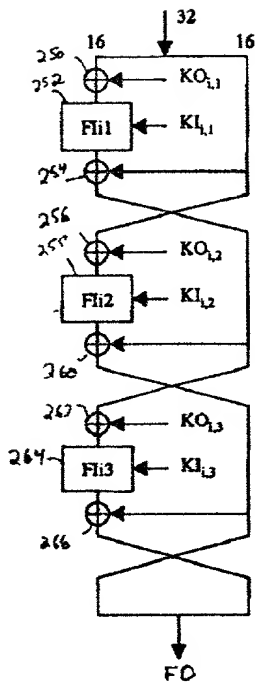
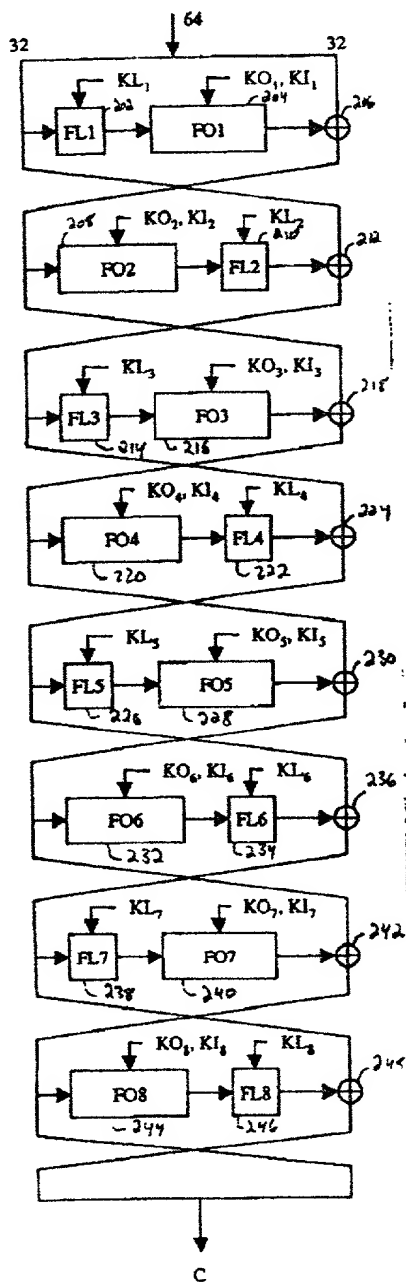


FIG. 1



bitwise AND operation
 bitwise OR operation
 one bit left rotation

200

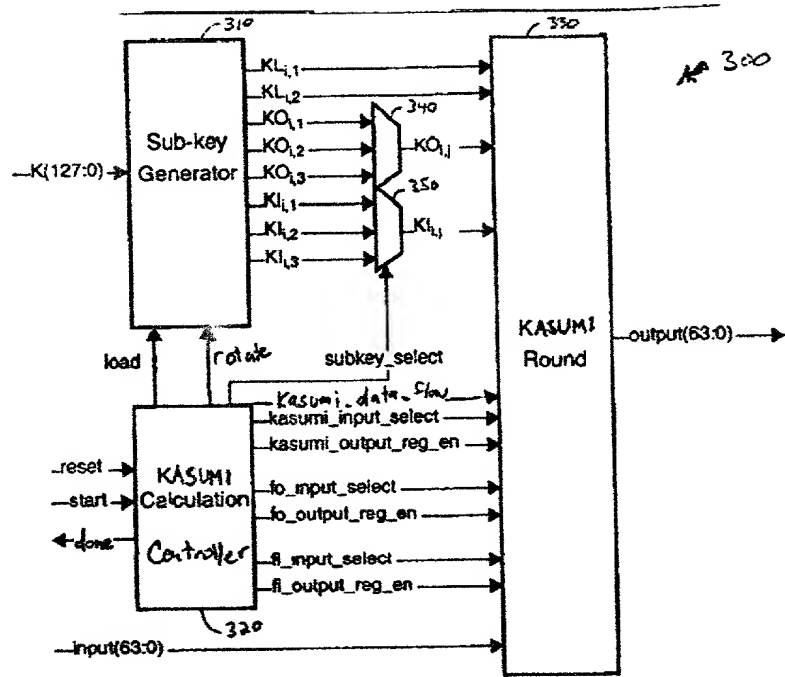
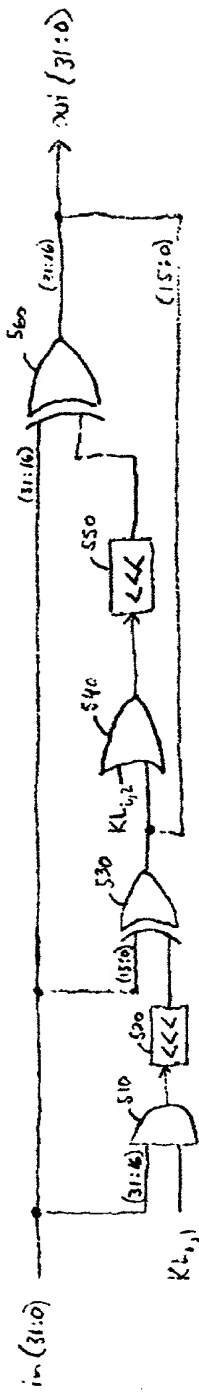


Fig. 3

—



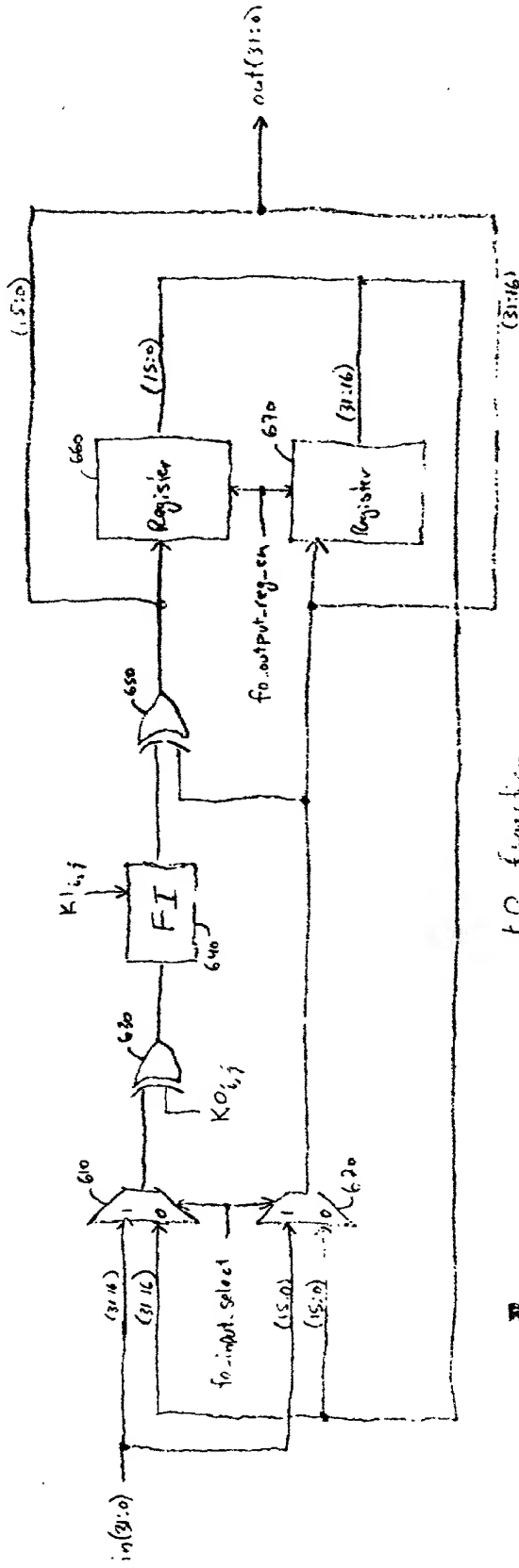
KASUMI Round
Fig. 4



FL Function

Fig. 5

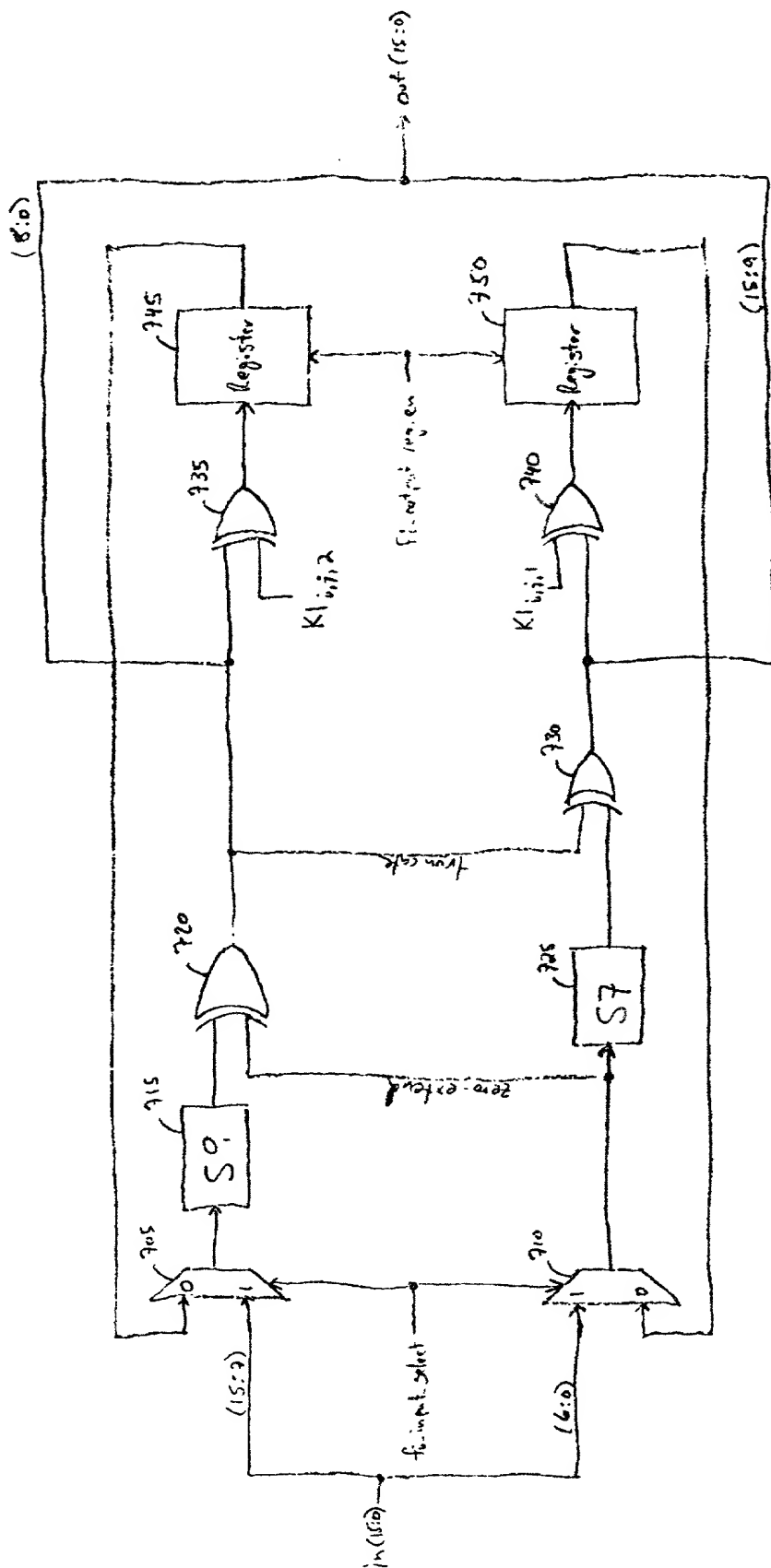
500



FO function

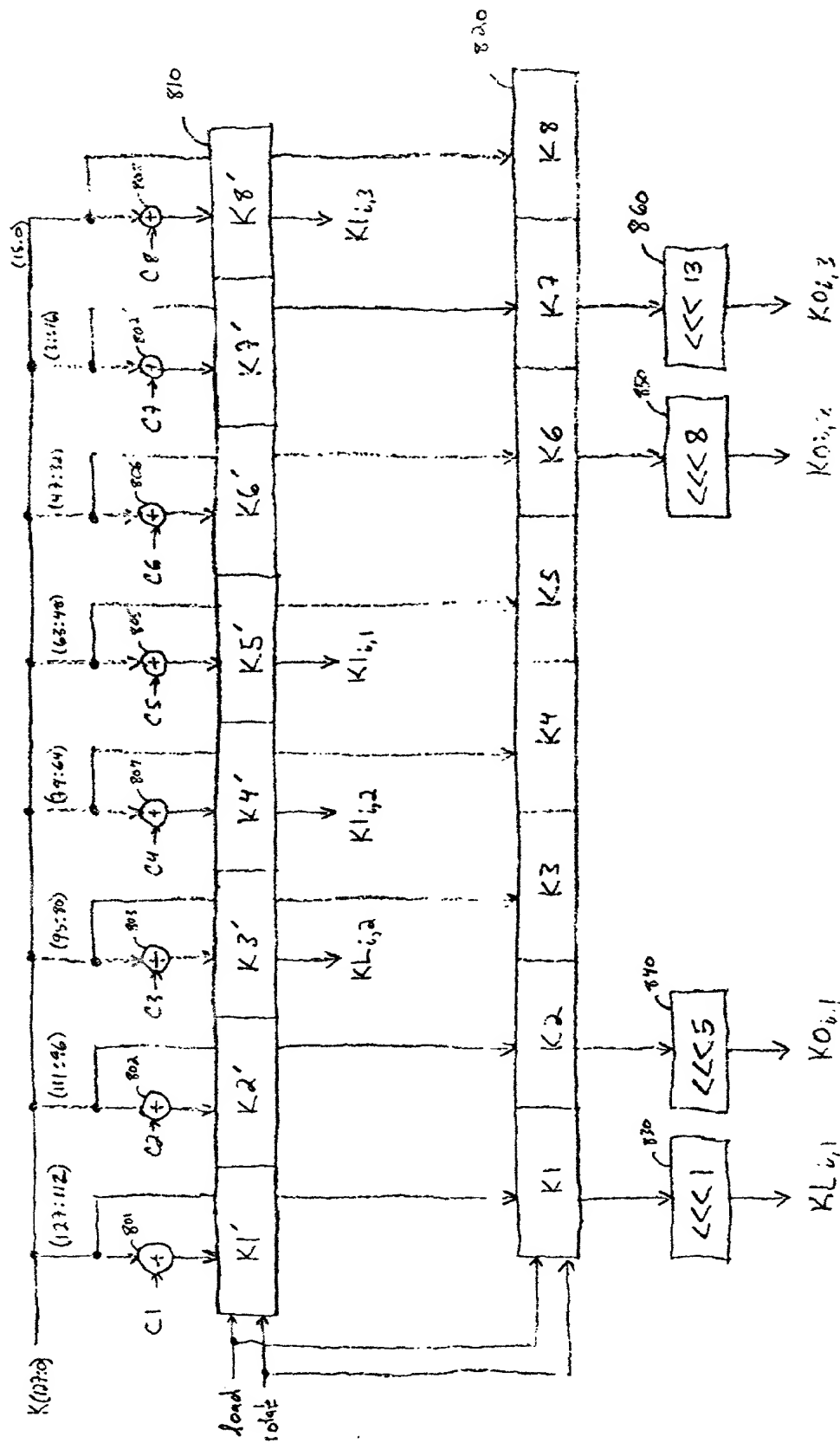
Fig. 6

600



F1 Function

Fig. 7



Sub-key Generator
Fig. 2

0920784.080101
T0T080"48202660

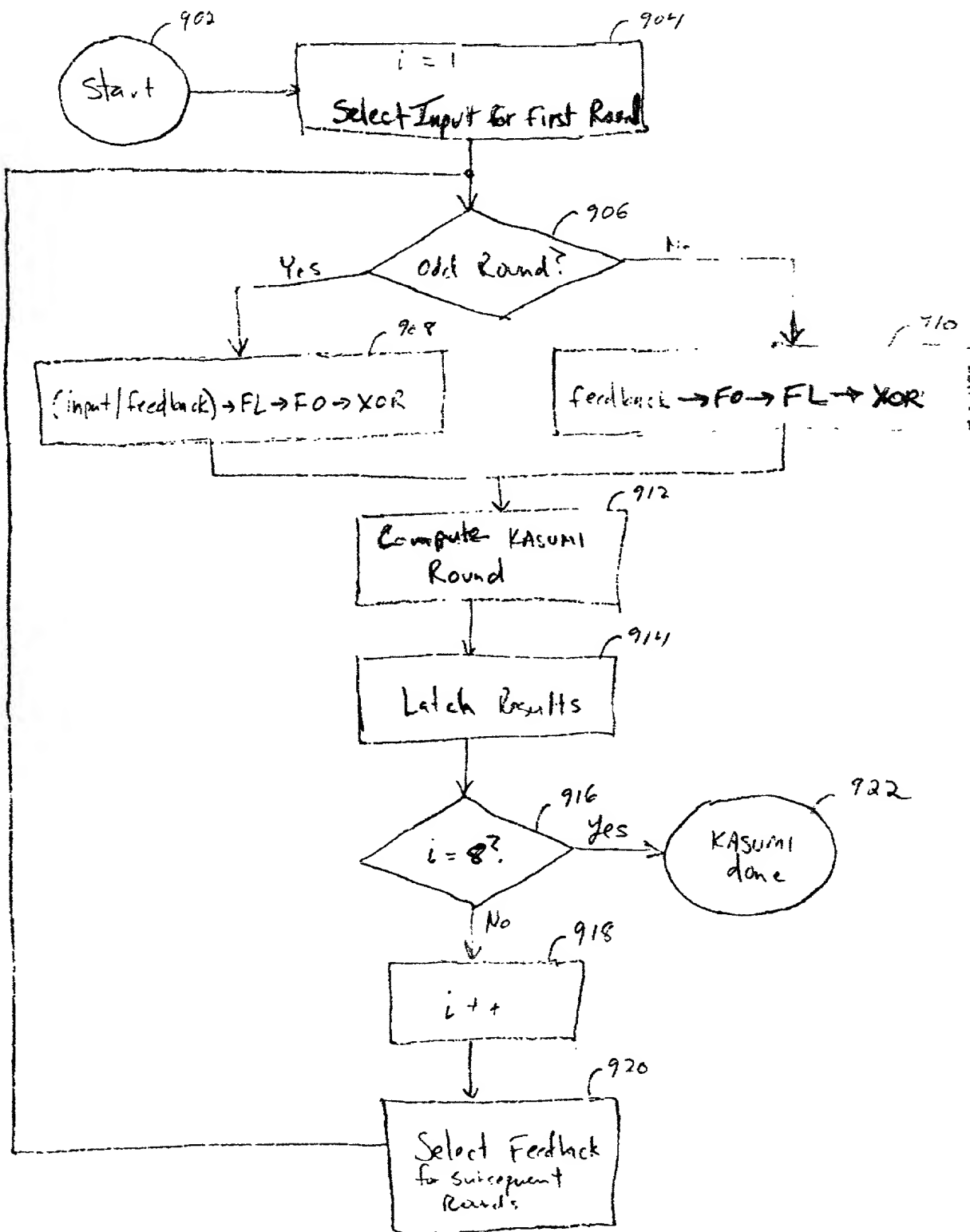


FIG. 9

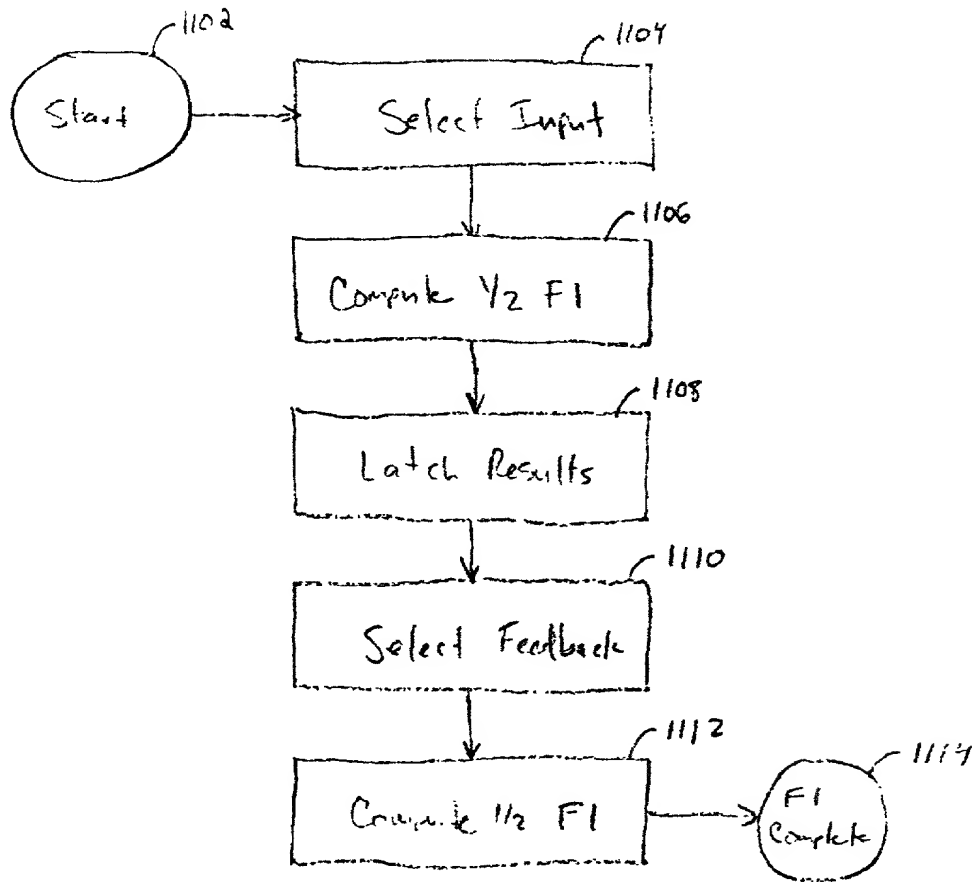


FIG. 11

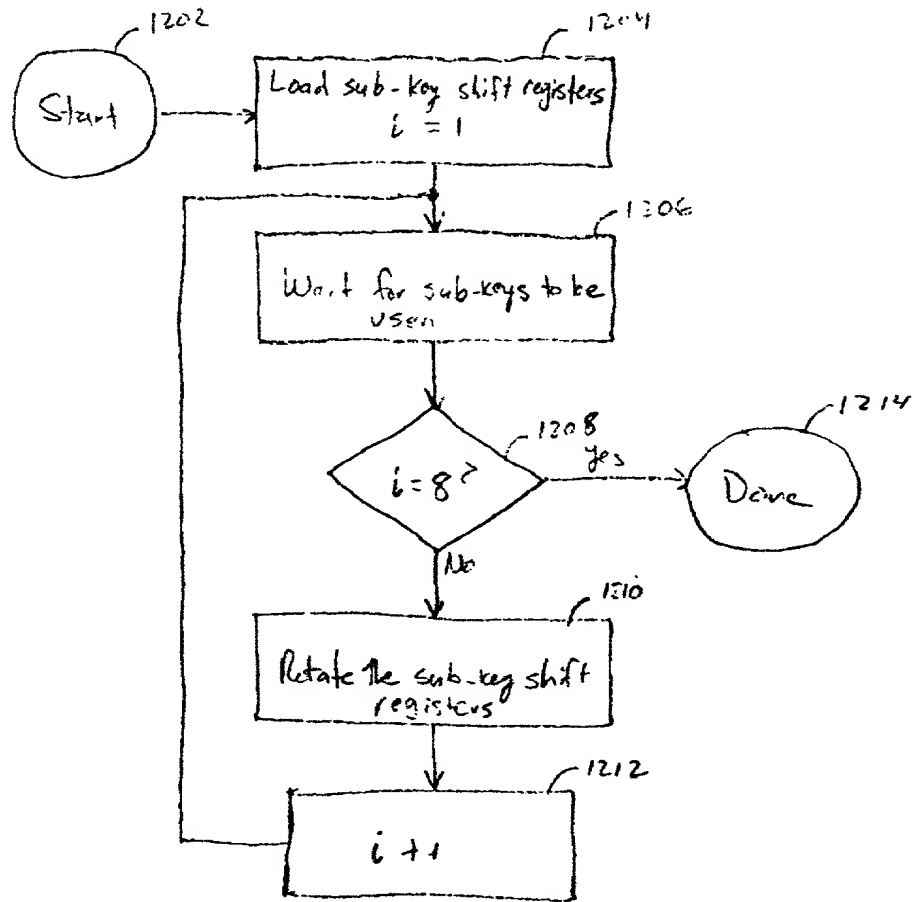


FIG. 12

0920784.000171

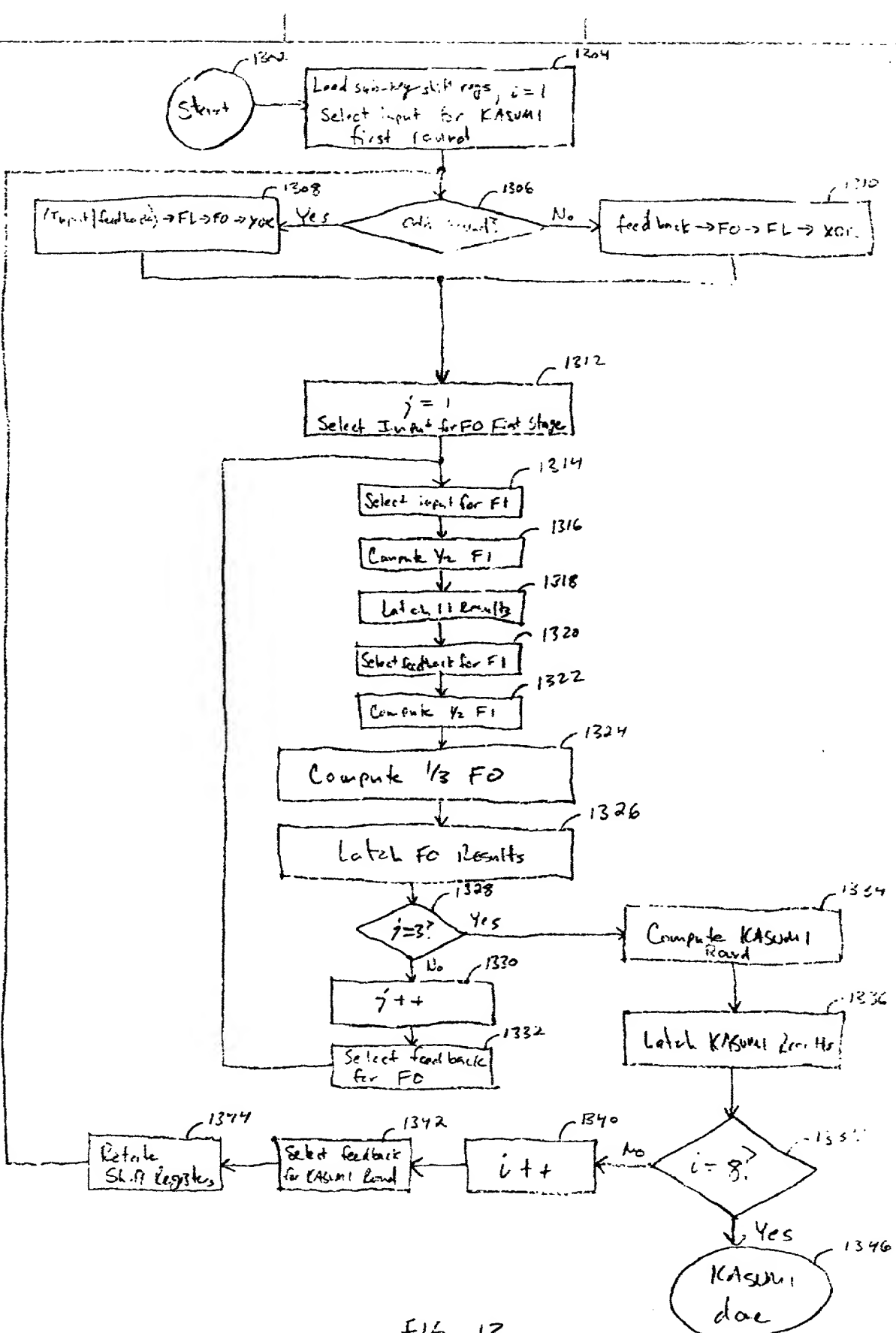


FIG. 13